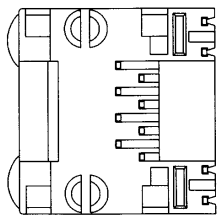


**PCB LAYOUT  
(Top View)**



**⚠ Note :** Total Cd(cadmium) Content< 5PPM(mg/kg)  
 Test Method : EN1122:2001 Method (ICP-AES)  
 : Total Pb(Lead) Content< 100PPM(mg/kg)  
 Test Method : US EPA3050B (ICP-AES)

**1. Electrical Char.**

- Insulation Resistance : 500M-Ohm Min.
- Contact Resistance : 20milli-Ohm Max.
- Rating Current : 1.5A Max.
- Rating Voltage : 120V AC.
- Dielectric Voltage : 1,000 Vrms 60 Sec. Min.

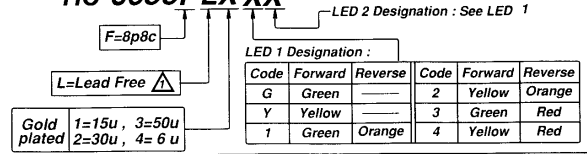
**2. Physical char.**

- Housing : Nylon (UL 94V-0)
- Color : Black Color
- Spring Wire : 0.35mm Thickness Phosphor Bronze Gold Plating Over Nickel
- Temperature Range : - 25 /+90 Deg C
- Durability : 500 Mating cycles Min.

**3. LED filings.**

- Forward DC Current : 2 mA
- Forward Voltage : 1.8 Volts Typical
- Reverse Voltage : 5 Volts Min.

**HC-0688FLX XX**



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**TITLE**

**RJ45 Side Entry PCB Jack With LED  
UL & cUL FILE NO. E131820**

**MODULAR JACKS ASSY FOR  
HC-0688FLX XX**

**SCALE SIZE DWG. NO. 9768-8L SERIES**

DWG. NO. **AS068005-L**

NOT SCALED DRAWING	TOLERANCE UNLESS OTHERWISE NOTICE	INCH	MM	DR. Steven	DATE
 THIRD ANGLE PROJECTION	LINEAR $\frac{MM}{INCH}$	00±0.01	00±0.25	CK'D	FEB-15 '05
		000±0.005	000±0.13	Johnn	FEB-15 '05
	ANGLE±0'30'	RADIL	+0.45 -0.15	APP'D Billy	FEB-15 '05